ALFINE-TIM

TECHNICAL INFORMATION
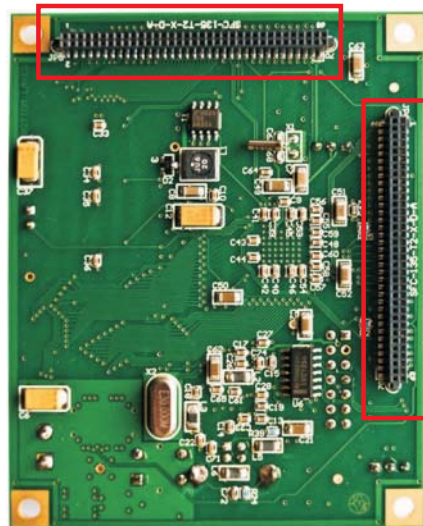
ALS-BF533 EVB *Core*BOARD

Black Fin™

Blackfin™ Processor Evaluation Board

The *Core*BOARD family is processor platform for building fully functional, Customer specific, easy expandable processor system. The *Core*BOARD includes all basic components necessary for working as standalone processor system, including reset and clocking circuitry, processor, non-volatile and SRAM memories, JTAG header and USB communication interface.

Board Features

■ **Processor**
Analog Devices Blackfin™ processor ADSP-BF533
(Core Clock: 500 MHz)

■ **Memory**
Non-volatile: 512kx8 Flash, 256kx16 MRAM
(Magentoresistive RAM); both of memories can serve as Boot Memory
Volatile: 256kx16 SRAM

■ **Communication Interfaces: JTAG, USB**
USB interface is powered by PlxTechnology NET2272 chip

■ **Expansion Headers**
High density; include all processor and system specific lines

■ **Power requirements**

Available Software and Development Tools

■ Supported by Analog Devices /CROSSCORE: VisualDSP++™, JTAG Emulators, device drivers and exmaple programs

System hardware and software specification can be changed without notices !

THE DSP COLLABORATIVE

ANALOG DEVICES